

<b>Notice of References Cited</b>	Application/Control No. 10/724,949	Applicant(s)/Patent Under Reexamination AURACHER ET AL.	
	Examiner Nelson Lam	Art Unit 2825	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,823,499	11-2004	Vasishta et al.	716/7
	B	US-5,825,202	10-1998	Tavana et al.	326/39
	C	US-2004/0243966	12-2004	Dellinger, Eric	716/017
	D	US-			
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	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
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	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Introduction to Actel FPGA Architecture", Actel Corporation Application Note, Sept. 1997.
	V	
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.